Compatibility Mode Pulse Signal Generation Circuit and Method; Colin Nayler et al.; 09/769,202

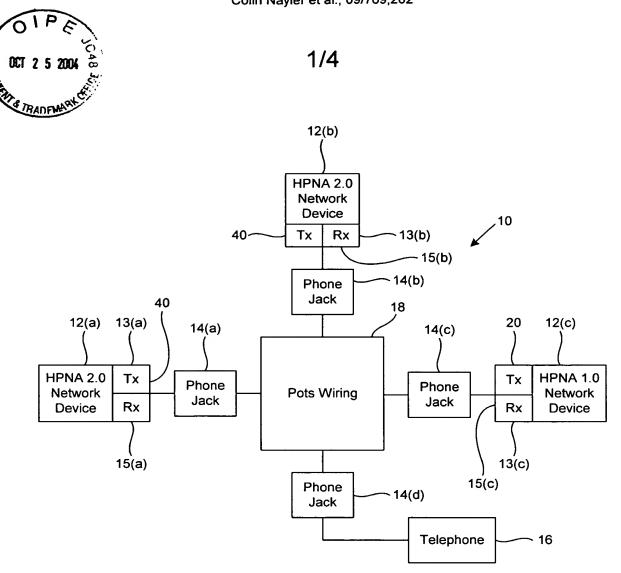
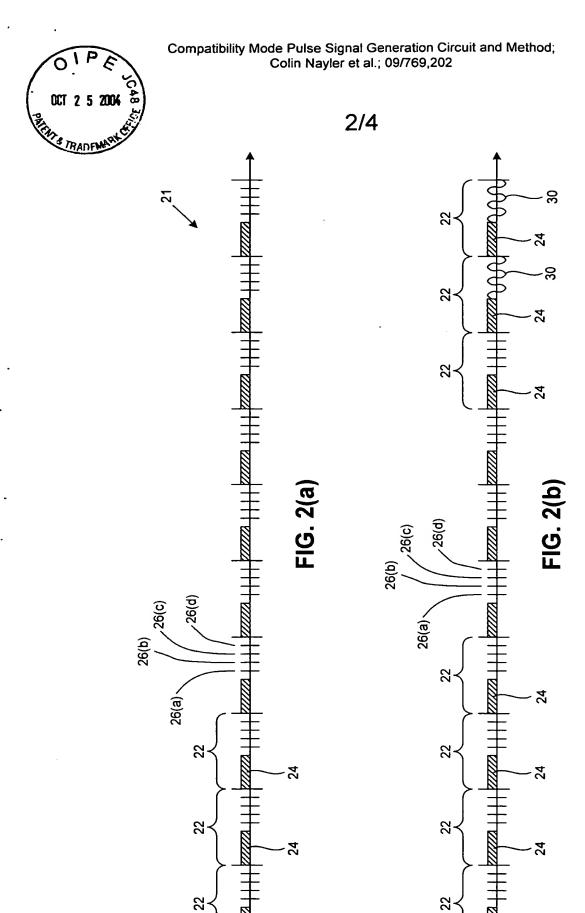
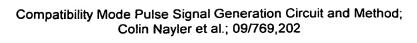


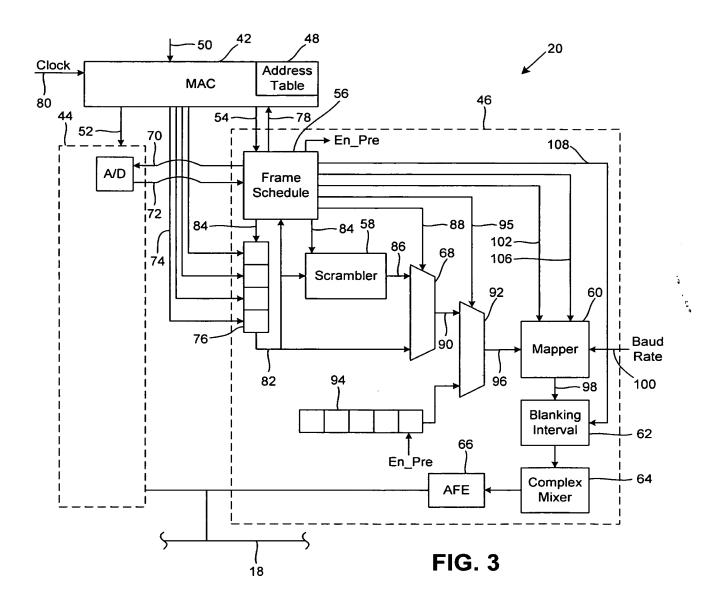
FIG. 1

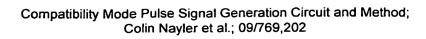






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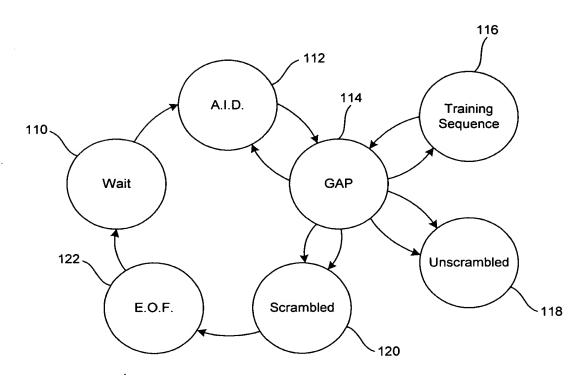


FIG. 4